

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

ELIYAHOU HARARI

Serial No.: 09/143,233

Filed: August 28, 1998

For: FLASH EEPROM SYSTEM
WITH DEFECTIVE BLOCK
SUBSTITUTION (as amended)



Group Art Unit: 2785

Examiner: L. Hua

San Francisco, California

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BH
3-24-00
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Assistant Commissioner of Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on March 17, 2000.

Sandy Ellsworth 3/17/00
Sandy Ellsworth Date

AMENDMENT AND REQUEST FOR DECLARATION OF INTERFERENCE

Sir:

Responsive to the Official Action mailed on December 20, 1999, setting a response period expiring on March 20, 2000, applicant responds to the Official Action as follows:

In the Claims:

Please add the following new claims:

--75. The memory as claimed in claim 69, further comprising an erasing circuit to erase contents of all of said memory cells accessible by a common sector address in response to an erasing signal.